



JPW
PATENT
8013-1074

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of:

Hidemitsu AOKI et al.

Confirmation No. 6696

Serial No. 09/715,000

GROUP 2825

Filed November 20, 2000

Examiner Chuong A. Luu

IMPROVED SEMICONDUCTOR WAFER SURFACE
AND METHOD OF TREATING A SEMICONDUCTOR
WAFER SURFACE

RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 22, 2004

Sir:

This responds to the Official Action of July 27, 2004.

Remarks begin on page 2 of this paper.

An **Appendix** is attached following the signature page of
this paper.